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SM 1 SM 1 EECE 488 - Set 1: Introduction and Background. EECE488: Analog CMOS Integrated Circuit Design Introduction and Background. Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca. Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged.

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Page 1 of 329. CORRECTIONS TO SOLUTIONS MANUAL. In the new edition, some chapter problems have been reordered and equations and figure refer- ences have changed. The solutions manual is based on the preview edition and therefore must be. corrected to apply to the new edition. Below is a list reflecting those changes.

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Behzad Razavi. Research and Teaching Interests: Analog, RF, mixed-signal integrated circuit design, dual-standard RF transceivers, phase-locked systems and frequency synthesizers, A/D and D/A converters, high-speed data communication circuits.

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Design of Analog CMOS Integrated Circuits . 2000. Abstract ... Iskander R, Dessouky M, Aly M, Magdy M, Hassan N, Soliman N and Moussa S Synthesis of CMOS Analog Cells Using AMIGO Proceedings of the conference on Design, Automation and Test in Europe: Designers' Forum - Volume 2.

Behzad Razavi | EE

Book Summary: The title of this book is Design of Analog CMOS Integrated Circuits and it was written by Behzad Razavi. This particular edition is in a Hardcover format. This books publish date is Aug 15, 2000. It was published by McGraw-Hill Education and has a total of 684 pages in the book.

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1) Introduction to Analog Design. 2) Basic MOS Device Physics. 3) Single-Stage Amplifiers. 4) Differential Amplifiers. 5) Current Mirrors and Biasing Techniques. 6) Frequency Response of Amplifiers. 7) Noise. 8) Feedback. 9) Operational Amplifiers. 10) Stability and Frequency Compensation. 11) Nanometer Design Studies. 12) Bandgap References

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